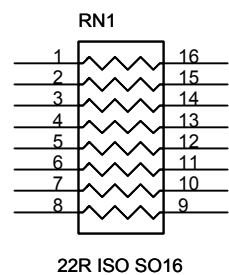


In TTL version (i.e. NC12MAXC32ST and NC12MAXC128ST), U4 is replaced with isolated resistor network, RN1. C12 - C16 not used.



Remove W1 if using PM0 for GPIO (not CAN).

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**Technological Arts, Inc.**

Title: **NanoCore12MAXC (40-pin modules)**

Size B	Document Number FILE: NanoCore12MAXR2.DSN	Rev 2
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